IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mike Liang

Serial No.:

08/909,312

Filed:

August 14, 1997

For:

Method For Distributing Connection Pads On A

Semiconductor Die

Group Art Un

2822

Examiner:

Picardat, K.

Atty Docket:

420099-654 / P-2880/1D

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assitant Commissioner for Patents, Washington D.C. 20231, on the date indicated below.

Maria Kovacs

Dec. 14, 1997

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CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

LSI Logic Coporation, a California corporation, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor(s) of the patent application identified above.

The assignment was recorded in the Patent and Trademark Office on December-12, 1996 at Reel 8306, Frame 617.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on infromation and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, inder Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Date:

Respectfully submitted,

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LSI LOGIC CORPORATION